

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Arup Bhattacharyya

Title: HIGH-PERFORMANCE ONE-TRANSISTOR MEMORY CELL

Docket No.: 1303.111US1

Filed: July 2, 2003

Examiner: Fazli Erdem



Serial No.: 10/612793

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Group Art Unit: 2826

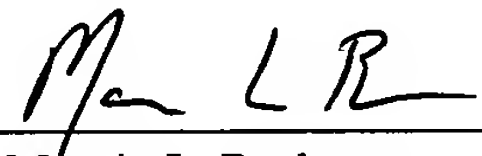
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

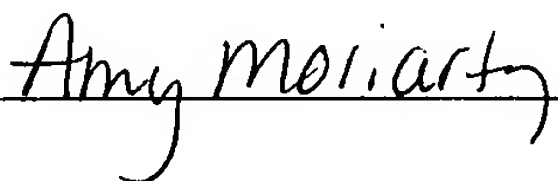
- ☒ A return postcard.
- ☒ Response to Restriction Requirement (1 Page).
- ☒ A Communication Concerning Related Applications (1 pg.).
- ☒ A Supplemental Information Disclosure Statement (1 pg.), Form 1449 (1 pg.), and copies of 1 cited documents.

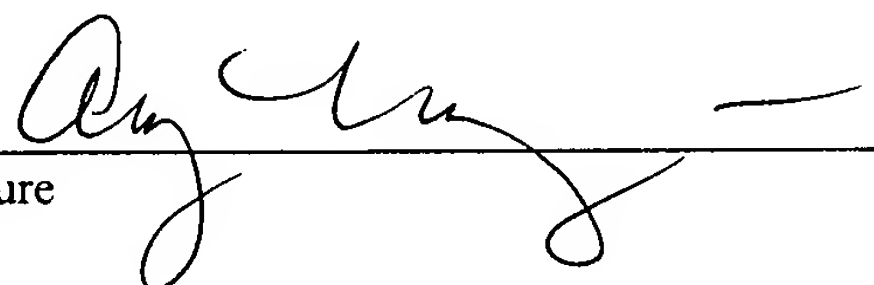
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 19th day of March, 2004.

  
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